

Analyzing Designs Using Model Technology's ModelSim & Altera's Quartus II Software

Course Length: 1 Day

Course Description

Note: this class focuses on the OEM version of ModelSim only.

In *Analyzing Designs Using Quartus II and Model Technology's ModelSim* you will learn how to perform design verification with these tools. Verification will include performing functional and timing simulation with ModelSim. The class will then focus on how to perform static timing analysis within Quartus II and how to optimize performance using Quartus II. You will also learn how to use the Quartus II Floorplan Editor to aid in improving design performance.

Skills Required

The following skills are strongly recommended:

- Completion of the *Designing with Synopsys' FPGA Express & Altera's Quartus II Software* course or *Designing with Exemplar's LeonardoSpectrum & Altera's Quartus II Software* course or *Designing with Quartus II* course or a good working knowledge of the Quartus II software.
- Background in digital logic design
- Experience with PCs and the Windows operating system
- Working knowledge of VHDL or Verilog (particularly testbenches)

Skills Developed

The skills developed in the course include:

- Performing functional simulations using ModelSim
- Performing timing simulations using ModelSim
- An understanding of static timing analysis
- An understanding of Quartus II-driven timing assignments
- An understanding of the Floorplan Editor