



Call for Tutorials

The Iberchip 2016 Organizing Committee invites proposals for half-day tutorials to be conducted on February 28, 2016, the first day of Iberchip 2016, held at Florianópolis, Brazil.

Tutorials are an integral function of Iberchip where instructors have the opportunity to share their expertise on a specific field/research with fellow researchers and those who wish to discover said field/research. We are soliciting proposals for topics of academic and industrial/application(s) interests. These topics may be established or it may be emerging – and with profound and/or new insights thereto, that is best articulated by means of an oral presentation. Most importantly, tutorials should be interesting with lively interaction with the attendees.

We encourage tutorial proposals submissions, including but not limited to:

- Analog Signal Processing
- Mixed-Mode Circuits
- Biomedical Electronics
- Communication/RF Circuits
- Wireless Sensor Networks
- Nanoelectronics
- Electronic Design Automation
- VLSI Systems and Applications
- FPGA Design and Applications
- Circuit Testing
- Fault Tolerant Circuits

Proposals, prepared in a one-column PDF file, should include the following information:

- Title of the tutorial
- Area of the tutorial (refer to the list above)
- 300 Word Abstract (for inclusion in registration materials)
- Keywords
- Instructor(s) and their credentials (2-page CV for each instructor)
- Previous offerings of tutorials, if any
- Learning objectives
- Target audience and prerequisite knowledge of audience
- Full description (1-2 pages to be used for evaluation)

Important Dates

Tutorial proposals due: October 24, 2015

Notification of acceptance: December 5, 2015

Complete Set of Tutorial Slides: December 21, 2015

Please submit proposals by email to the Tutorial Co-chairs:

Héctor Pettenghi (hector@eel.ufsc.br) and Gloria Huertas (Gloria@us.es) by the October 24, 2015 deadline.

